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Electronic Version v1.1

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Title of Invention

METHOD OF MANUFACTURING GATE, THIN FILM TRANSISTOR AND PIXEL

Application Number:

Date:

First Named Applicant:

Mr. Ta-Jung Su

Confirmation Number:

Attorney Docket Number:

13129-US-PA

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
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us-assignment	13129-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	13129assignment01.tif
	13129assignment02.tif
us-request	13129-usrequ.xml
·	us-request.dtd
	us-request.xsl
us-fee-sheet	13129-usfees.xml
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application-body	13129usf.xml
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	mathml2.dtd
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	us-application-body.xsl
	wipo.ent
application-body-pdf-wrap	13129usf-pdf-wrap.xml
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Comments

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